

FIG. 1
(Prior Art)

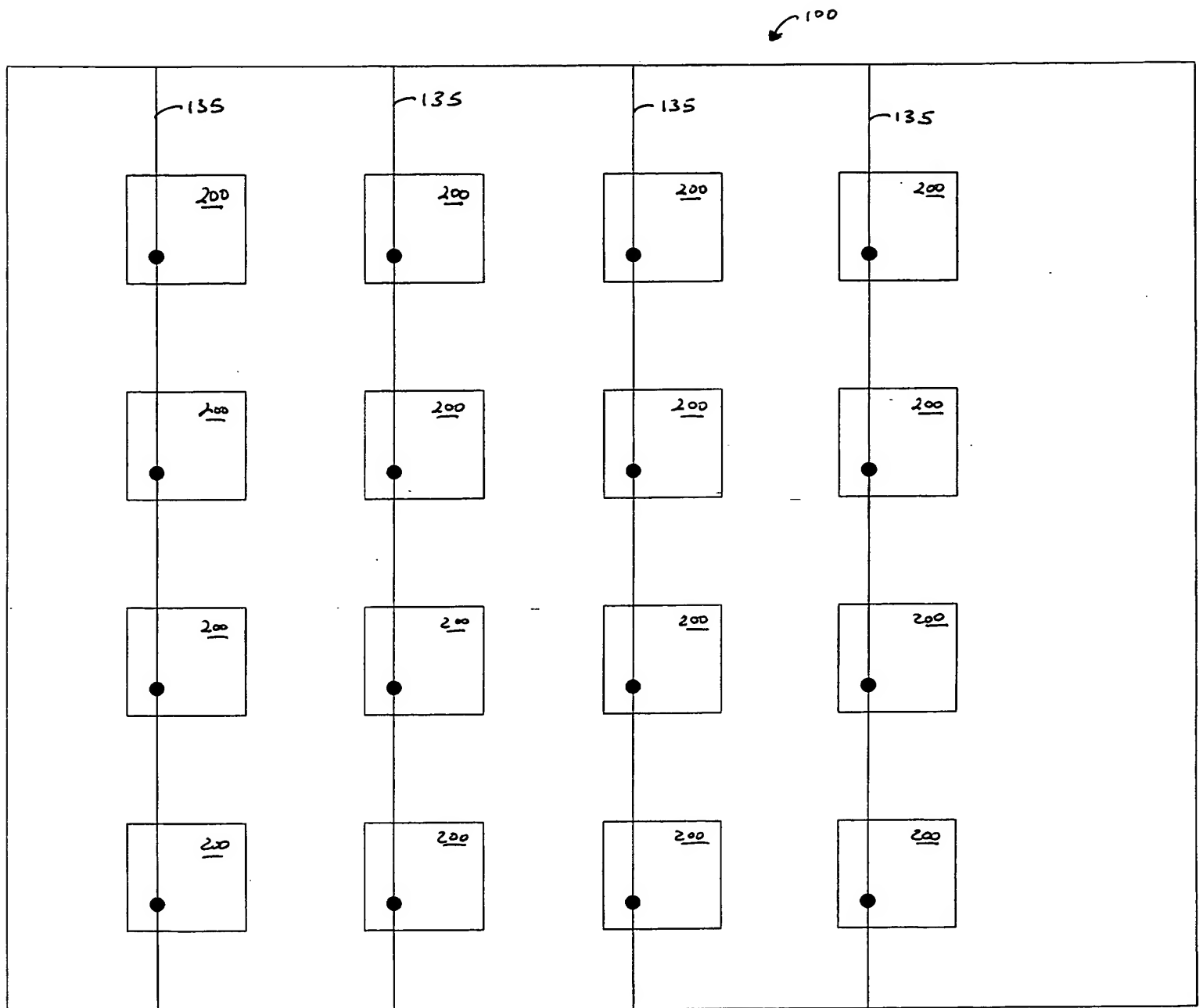


FIG. 2A
(Prior Art)

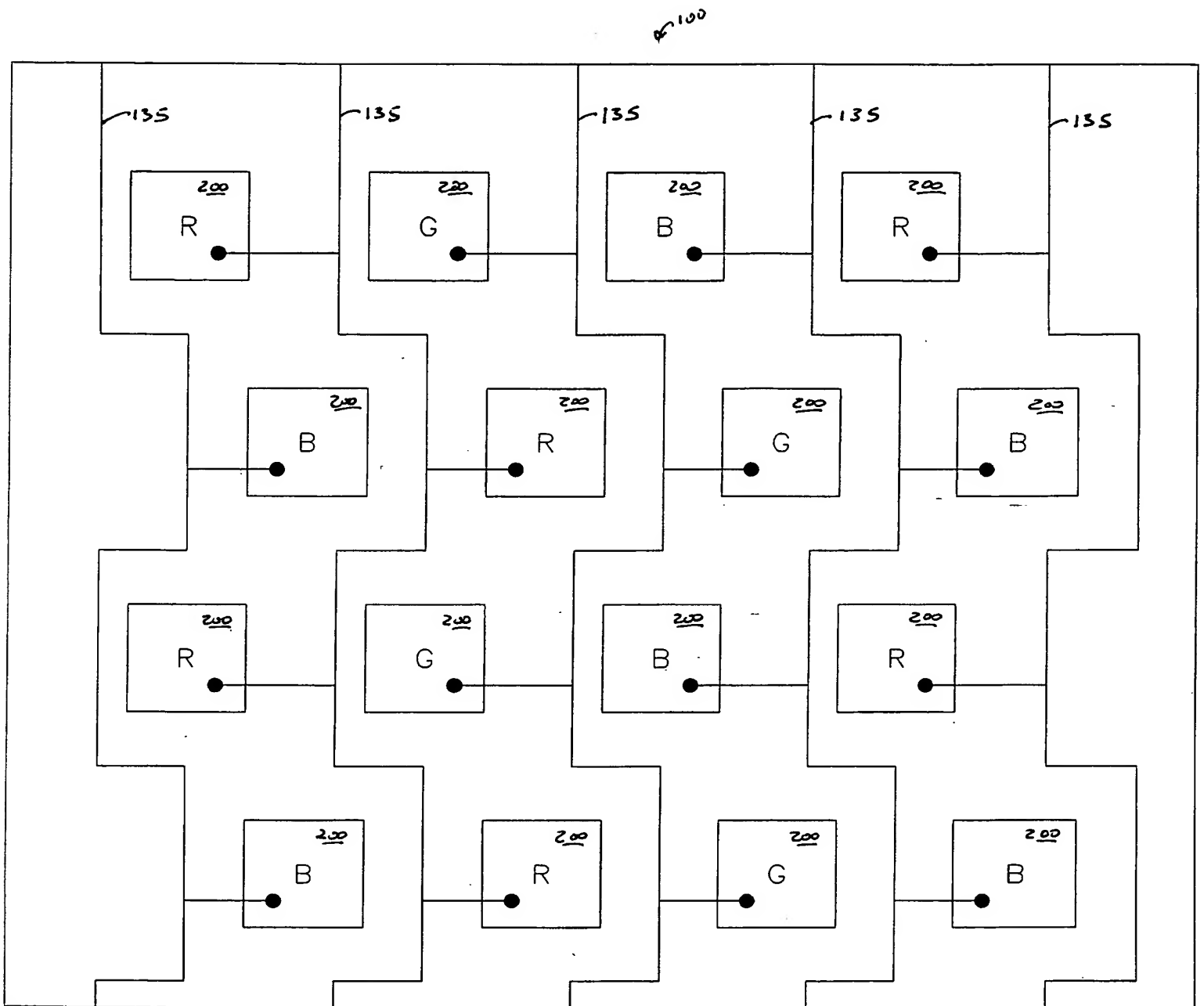


FIG. 2B
(PREF. AAT)

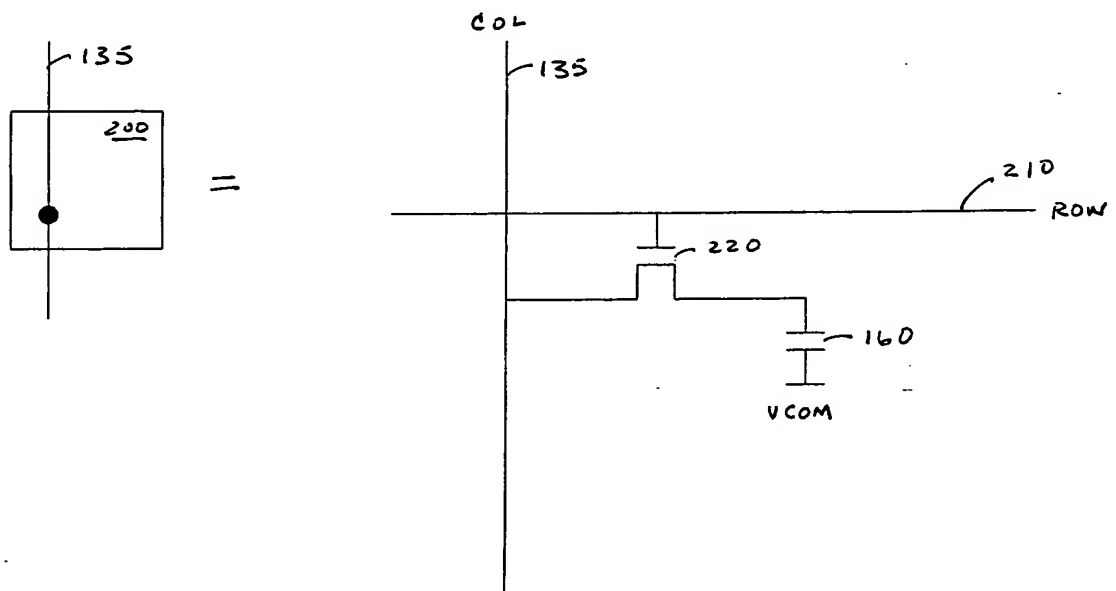


FIG. 2C
(Prior Art)

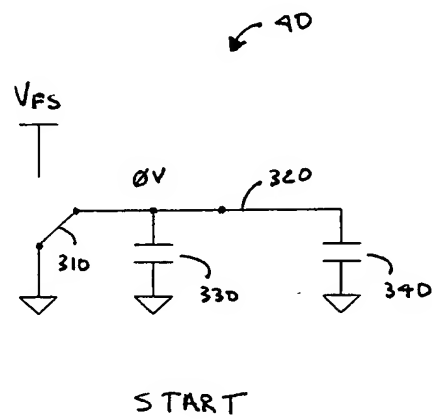
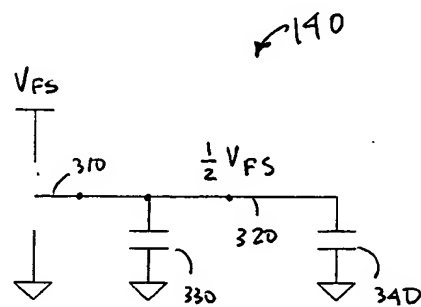


FIG. 3A
(Prior Art)

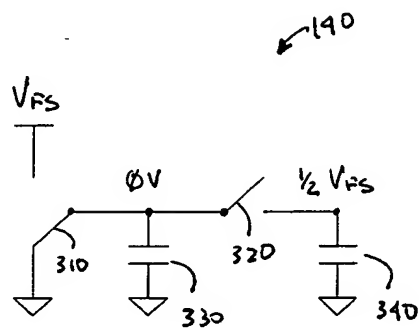
$$L_{SB} = 1$$

(Order Art)



EQUALIZE

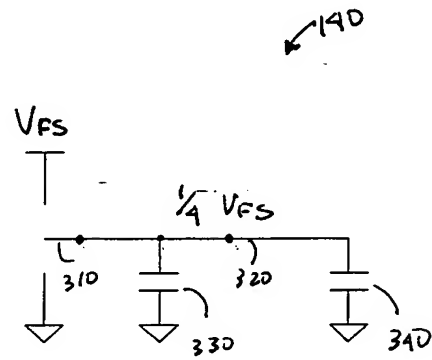
FIG. 3C
(Prior Art)



$$Bit_i = \phi$$

FIG. 3D

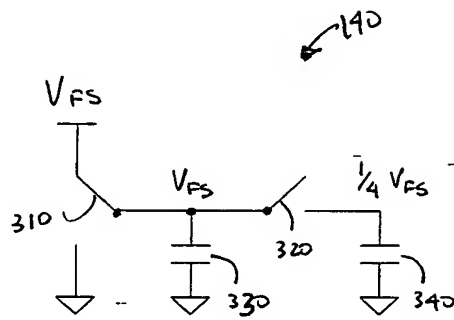
(Prior Art)



EQUALIZE

FIG. 3E

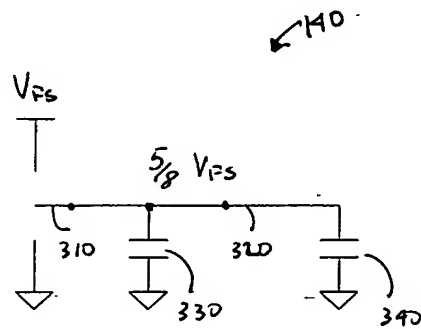
(Prior Art)



$$\text{Bit}_2 = 1$$

FIG. 3F

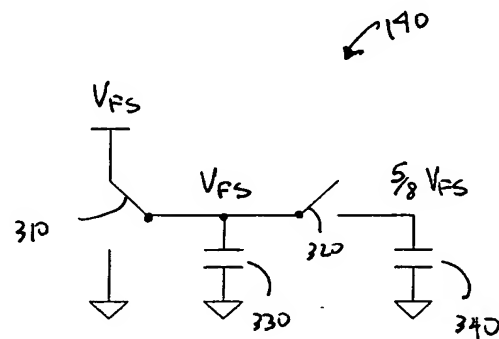
(Prior Art)



EQUALIZE

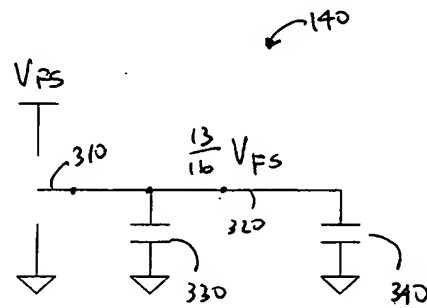
FIG. 3G

(prior art)



MSB = 1

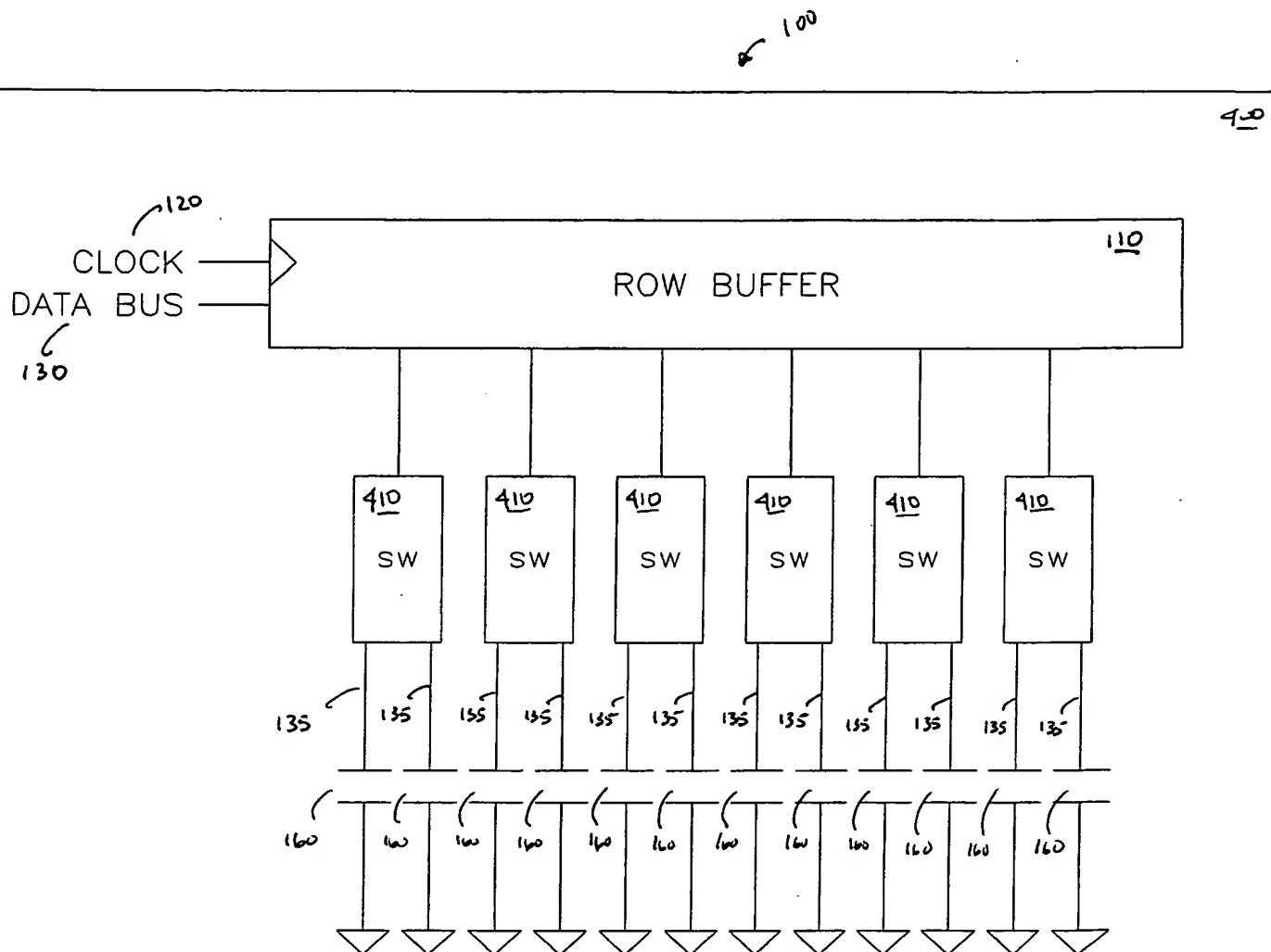
FIG. 3H
(Prior Art)



Done

FIG. 3 I

(Prior Art)



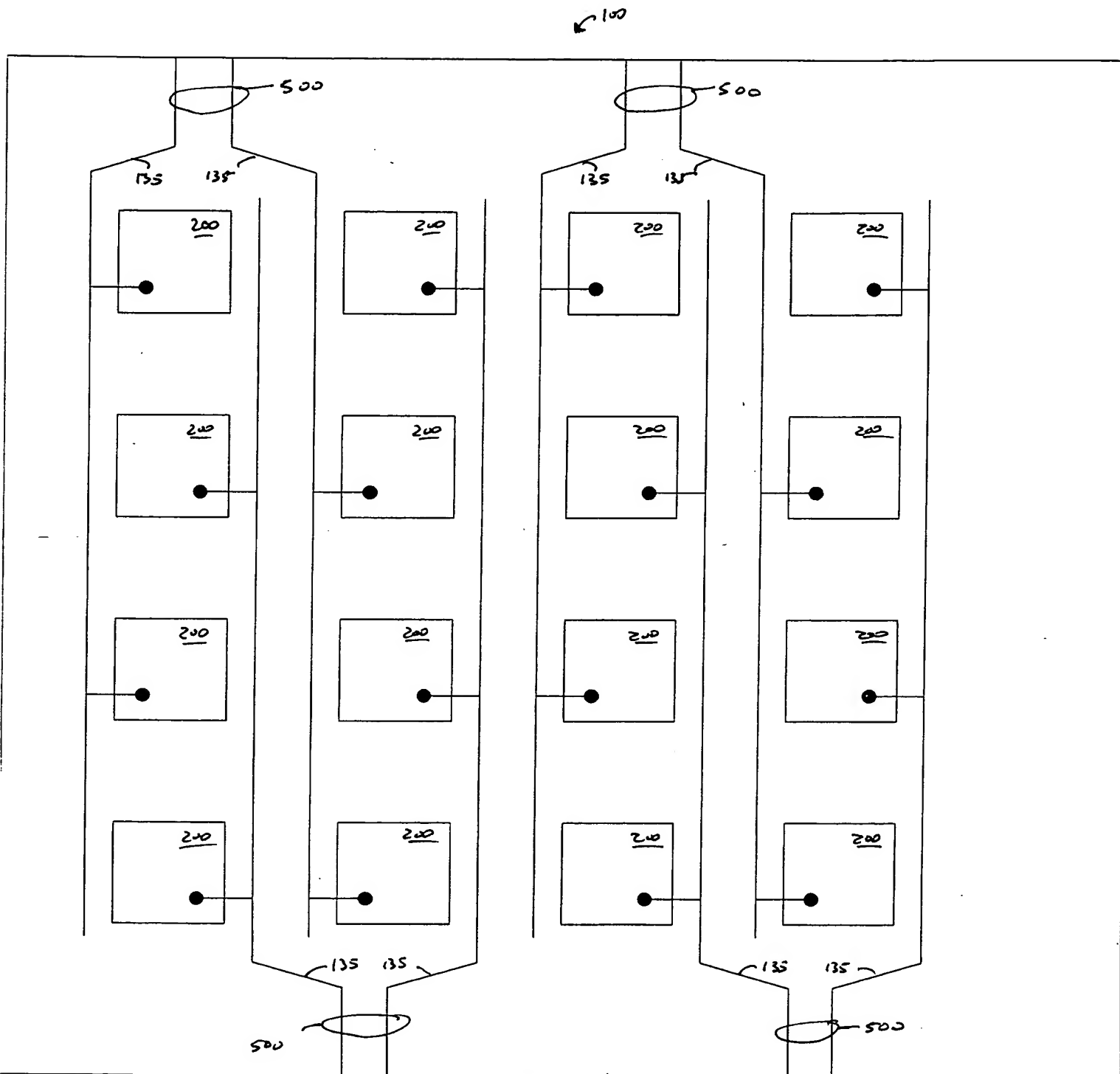


FIG. 5A

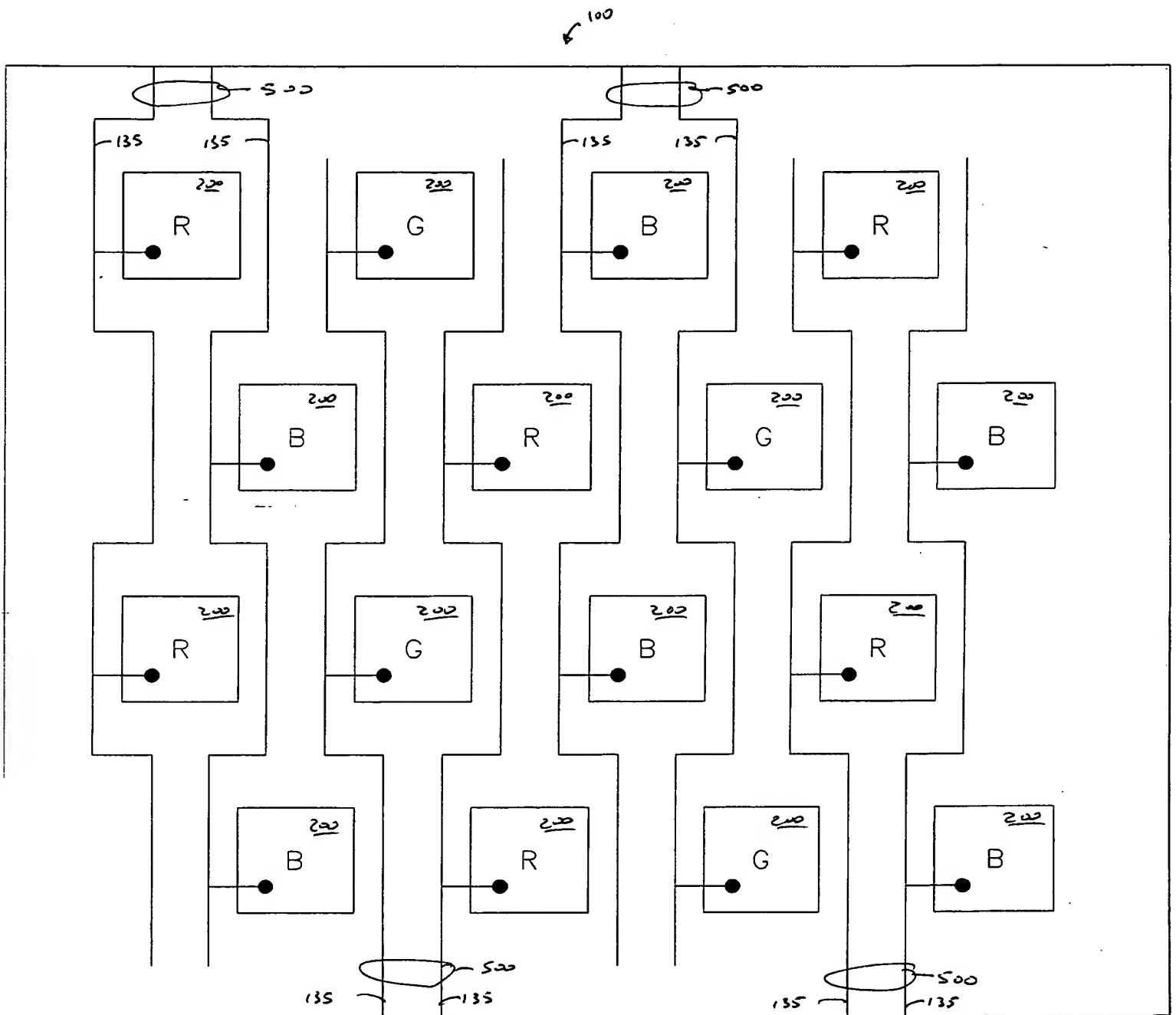


FIG. 5B

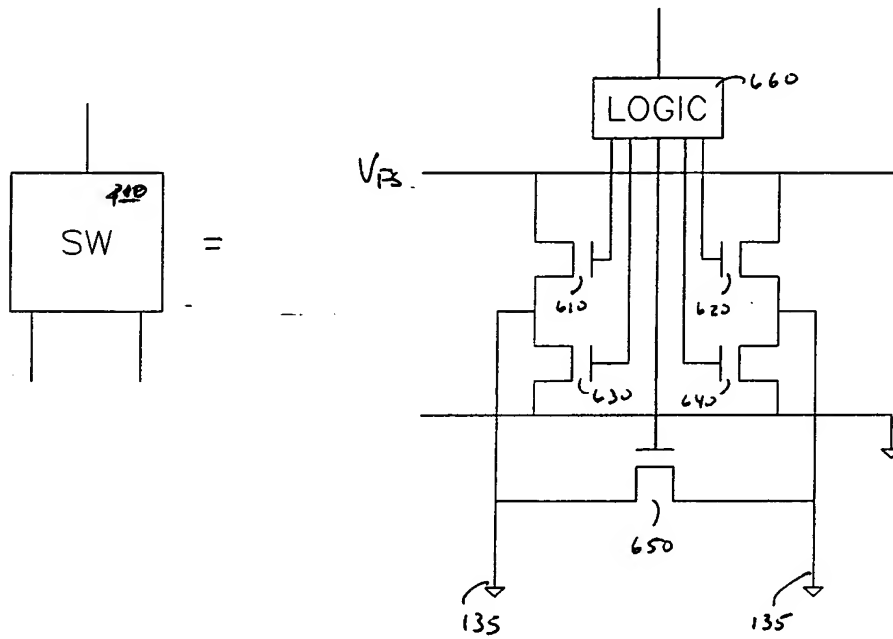


FIG. 6